

Inventor: Jigish G. Trivedi

Title: Methods of Forming a Conductive Line, and Integrated Circuitry

Assignee: Micron Technology, Inc.

#2/105  
Dking  
10/5/01

1c978 U.S. PTO  
09/976635  
10/12/01

**INFORMATION DISCLOSURE STATEMENT**


**References – See Attached Form PTO-1449**

The citations listed, copies attached, may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR § 1.56. The Examiner is requested to make these citations of official record in this application. No admission is made regarding whether all the submitted references are prior art.


The materials cited are presented to assist in and expedite examination of this application. The present invention is considered to be patentable over the cited materials. Expeditious examination and allowance of this application as a patent are therefore urged in order that the public may benefit from the disclosure and commercialization of the invention.

Respectfully submitted,

Dated: 10/08/2001

Inventor:   
Jigish G. Trivedi

Dated: 10-12-01

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